



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE

(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.

కాకతీయ ప్రాధ్యోగికీ एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत

కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015. తెలంగాణ రాష్ట్రం, భారతదేశము

Office of the Controller of Examinations

Only for SUPPLEMENTARY

2018, 2019, 2020 & 2021 Admitted Batches

No. URR18- B.Tech. III-Sem. / ESE Supply Schedule / 2023

Date: 10-11-2023

Revised End Semester Examinations (ESE) of URR-18 B.Tech. III-Semester SUPPLEMENTARY Examinations, November/December, 2023

Ref: Circular No: KITS/Acad/ Circular/ UG-2023/221, Dated 28.11.2023.

REVISED TIME TABLE

Examination Timings: 10:00 AM to 01:00 PM

Date & Day of Examination	CE	ME	EIE	EEE	ECE	CSE	IT	CSN	ECI	CSO (IoT)	CSM (AIML)
29.11.2023 Wednesday	Engineering Mathematics-III	Engineering Mathematics - III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III	Engineering Mathematics-III
04.12.2023 Monday	Fluid Mechanics	Material Science and Metallurgy	Electrical and Electronic Measurements	Electromagnetic Fields	Signals & Systems	Discrete Mathematics	Discrete Mathematics	Database Management Systems	Electronic Measurements and Sensors	Fundamentals of Internet of Things	Formal Languages and Automata Theory
06.12.2023 Wednesday	Surveying	Engineering Thermodynamics	Sensors and Transducers	Network Theory	Network Analysis	Computer Architecture and Organization	Computer Architecture and Organization	Computer Architecture and Organization	Signals Systems and Random Processes	Computer Organization and Architecture	Computer Organization and Architecture
08.12.2023 Friday	Construction Materials	Machine Drawing	Electronic Devices and Circuits-I	Electronic Devices and Circuits	Analog Circuits - I	Advanced Data Structures	Advanced Data Structures	Advanced Data Structures	Electronic Devices and Applications	Advanced Data Structures	Advanced Data Structures
12.12.2023 Tuesday	-	-	Digital Circuits and Logic Design	-	Switching Theory & Logic Design	Digital Electronics	Switching Theory and Logic Design	Digital Electronics	Digital Circuits and Logic Design	Digital Electronics	-
14.12.2023 Thursday	-	-	-	-	-	-	Operating Systems	Operating Systems	-	Computer Networks	Operating Systems
15.12.2023 Friday	-	Essence of Indian Traditional Knowledge	-	-	-	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	-	Essence of Indian Traditional Knowledge	-

Examination Timings: 02:00 pm to 05:00 pm

16.12.2023 Saturday Revised Date	Strength of Materials	Mechanics of Solids	Object Oriented Programming	Electrical Measurements & Instrumentation	Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through C++	Object Oriented Programming through JAVA	Object Oriented Programming	Object Oriented Programming through JAVA	Object Oriented Programming through JAVA
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Candidate should occupy his/her seat atleast five minutes before the commencement of the examination

To: All Notice Boards/ Class WhatsApp groups

Copy to:- 1. The Principal for information

2. HOD / BoS Chairpersons

3. Dean, Academic Affairs

4. Prof. I/C. Training & Placement

5. Web Team

Controller of Examinations